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**EORM PTO-1449** U.S. DEPARTMENT OF COMMERCE ATTY. DOCKET NO. SERIAL NO. (REV. 7-80) PATENT AND TRADEMARK OFFICE 030318 10/600,231 INFORMATION DISCLOSURE STATEMENT BY APPLICANT **APPLICANT** (Use several sheets if necessary) FAN et al. **FILING DATE** GROUP DATE MAILED: March 21, 2005 6/19/2003 2663 **U.S. PATENT DOCUMENTS** \*EXAMINER **SUB** INITIAL DOCUMENT NUMBER DATE CLASS NAME **CLASS** /SH/ A1 6,628,924 9/30/2003 Miyamoto /SH/ 6,317,600 A2 11/13/2001 Salonaho et al. **A3** A4 Α5 **A6** Α7 U.S. PATENT APPLICATION PUBLICATION DOCUMENTS \*EXAMINER PUB. **SUB** INITIAL **PUBLICATION NUMBER** DATE NAME CLASS **CLASS** Bl B<sub>2</sub> FOREIGN PATENT DOCUMENTS **EXAMINER** Ref INITIAL DATE COUNTRY DOCUMENT NUMBER NAME **CLASS** No /SH/ 2000-307511 CI 11/2/2000 JP **NEC Corporation** C2 C3 C4 C5 OTHER PRIOR ART (Including Author, Title, Date, Pertinent Page, Etc.)

/SH/	DI	Attar et al., "A Reverse Link Outer-Loop Power Control Algorithm for cdma2000 1xEV Systems," 2002 IEEE INTERNATIONAL CONFERENCE ON COMMUNICATIONS, CONFERENCE							
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